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SPECIFICATIONS No.	. 14TLM101

Issue: May. 29, 2012

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### APPENDIX

### 1. APPLICATION

This Specification is applicable to 8.94cm (3.5 inch) Blanview TFT-LCD back-light monitor for non-military use.

- © ORTUS TECHNOLOGY makes no warranty or assume no liability that use of this Product and/or any information including drawings in this Specification by Purchaser is not infringing any patent or other intellectual property rights owned by third parties, and ORTUS TECHNOLOGY shall not grant to Purchaser any right to use any patent or other intellectual property rights owned by third parties. Since this Specification contains ORTUS TECHNOLOGY's confidential information and copy right, Purchaser shall use them with high degree of care to prevent any unauthorized use, disclosure, duplication, publication or dissemination of ORTUS TECHNOLOGY'S confidential information and copy right.
- If Purchaser intends to use this Products for an application which requires higher level of reliability and/or safety in functionality and/or accuracy such as transport equipment (aircraft, train, automobile, etc.), disaster-prevention/security equipment or various safety equipment, Purchaser shall consult ORTUS TECHNOLOGY on such use in advance.
- O This Product shall not be used for application which requires extremely higher level of reliability and/or safety such as aerospace equipment, telecommunication equipment for trunk lines, control equipment for nuclear facilities or life-support medical equipment.
- ◎ It must be noted as an mechaniacl design manner, especial attention in housing design to prevent arcuation/flexureor caused by stress to the LCD module shall be considered.
- ORTUS TECHNOLOGY assumes no liability for any damage resulting from misuse, abuse, and/or miss-operation of the Product deviating from the operating conditions and precautions described in the Specification.
- ORTUS TECHNOLOGY is not responsible for any nonconformities and defects that are not specified in this specifications.
- © If any issue arises as to information provided in this Specification or any other information, ORTUS TECHNOLOGY and Purchaser shall discuss them in good faith and seek solution.
- ORTUS TECHNOLOGY assumes no liability for defects such as electrostatic discharge failure occurred during peeling off the protective film or Purchaser's assembly process.
- ◎ This Product is compatible for RoHS directive.

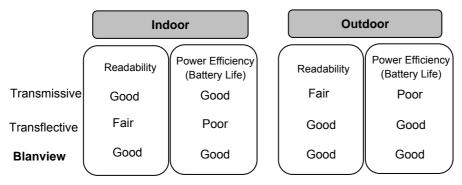
Object substance	Maximum content [ppm]
Cadmium and its compound	100
Hexavalent Chromium Compound	1000
Lead & Lead compound	1000
Mercury & Mercury compound	1000
Polybrominated biphenyl series (PBB series)	1000
Polybrominated biphenyl ether series (PBDE series)	1000

### SPECIFICATIONS No. 14TLM101

### 2. OUTLINE SPECIFICATIONS

### 2.1 Features of the Product

- 3.5" diagonal with resolution of 720[H]x320[V] dots.
- 6-bit 262,144 color display capability.
- Single power supply operation of 3V.
- Timing generator (TG), Counter-electrode driving circuitry, Built-in power supply circuit.
- Long life & High brightness LED back-light and Touch panel operation monitor.
- Blanview TFT-LCD, improved outdoor readability.



2.2 Display Method

Items	Specifications	Remarks
Display type TN type 262,144 Colors.		
	Blanview, Normally white.	
Driving method	a-Si TFT Active matrix.	
	Line-scanning, Non-interlace.	
Dot arrangement	RGB stripe arrangement.	Refer to Fig. 1
Input signal type	6-bit RGB, parallel input.	
Backlight	Long life & High bright white LED.	
Touch panel	Resistance type, transmissive analog tablet	

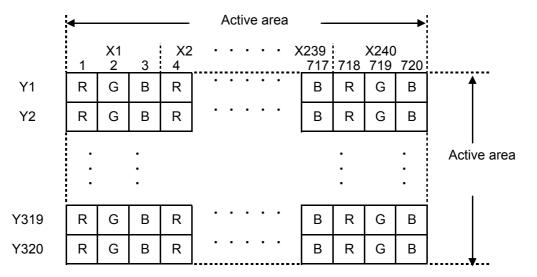
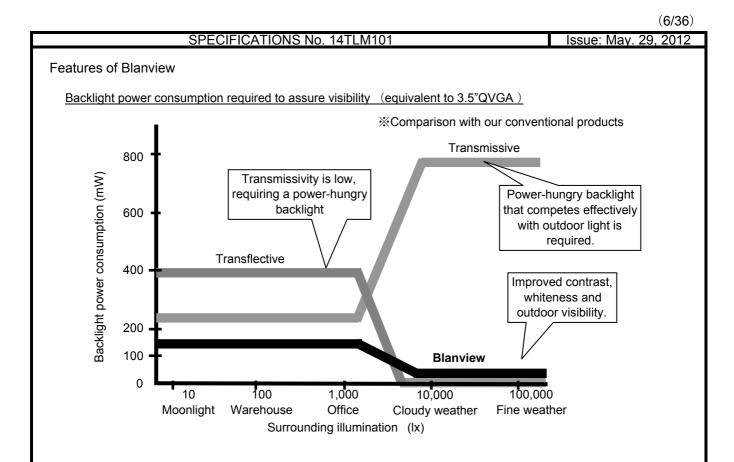


Fig. 1 Dot arrangement (FPC cable placed leftside)

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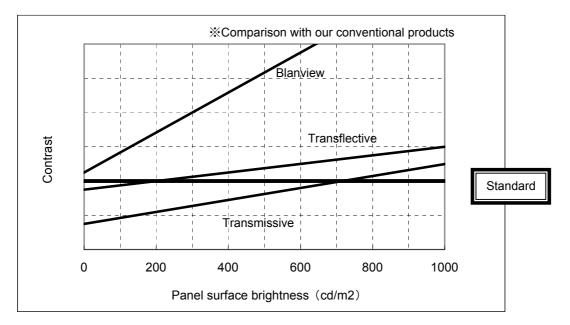
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#### Contrast characteristics under 100,000lx (same condition as direct sunlight.)

With better contrast (hgher contrast ratio), Blanview TFT-LCD has the best outdoor readability in three different types of TFT-LCD.

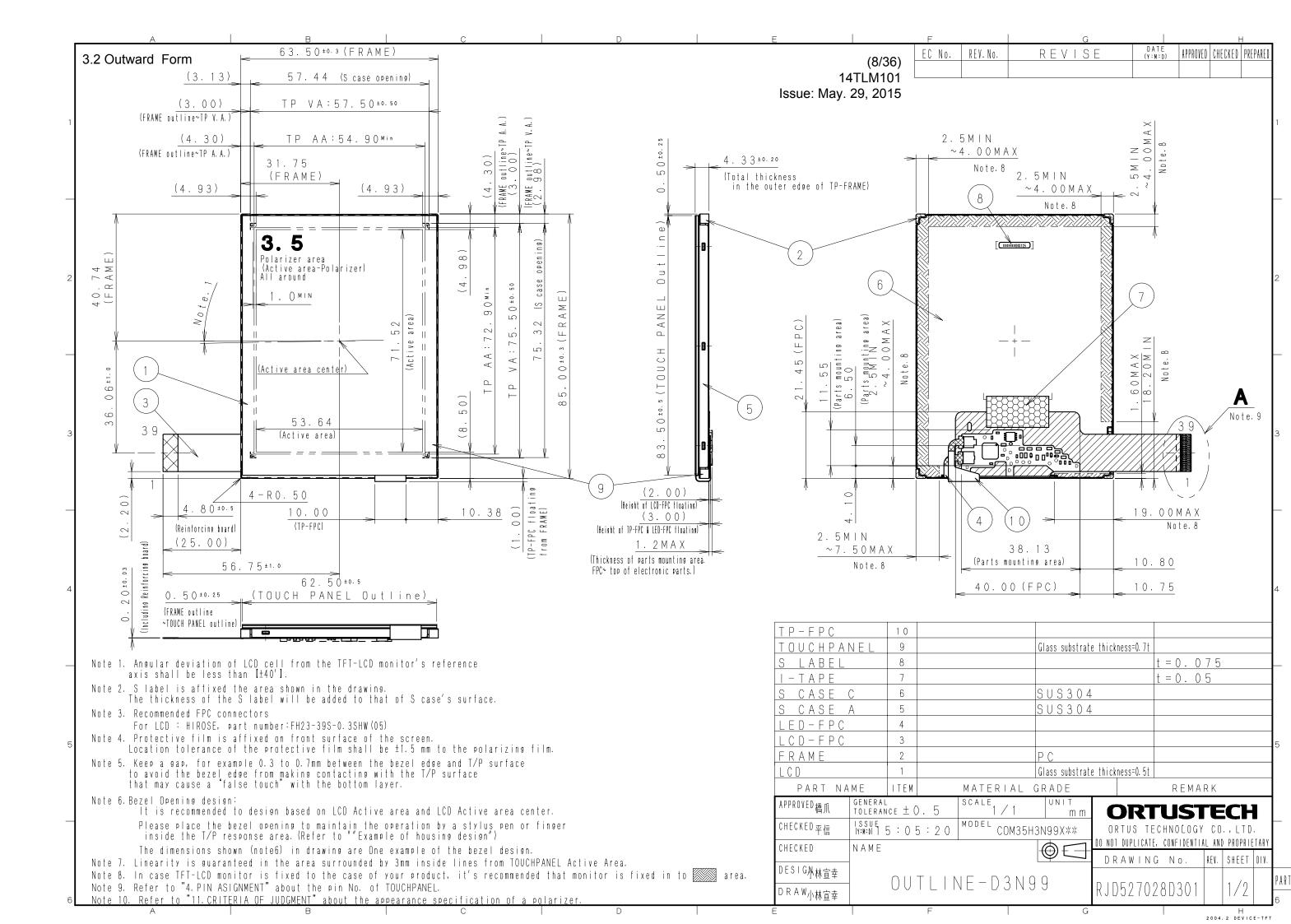
Below chart shows contrast value against panel surface brightness. (Horizontal: Panel surface brightness/ Vertical: Contrast value) LCD panel has enough outdoor readability above our Standard line. (ORTUS TECHNOLOGY criteria)

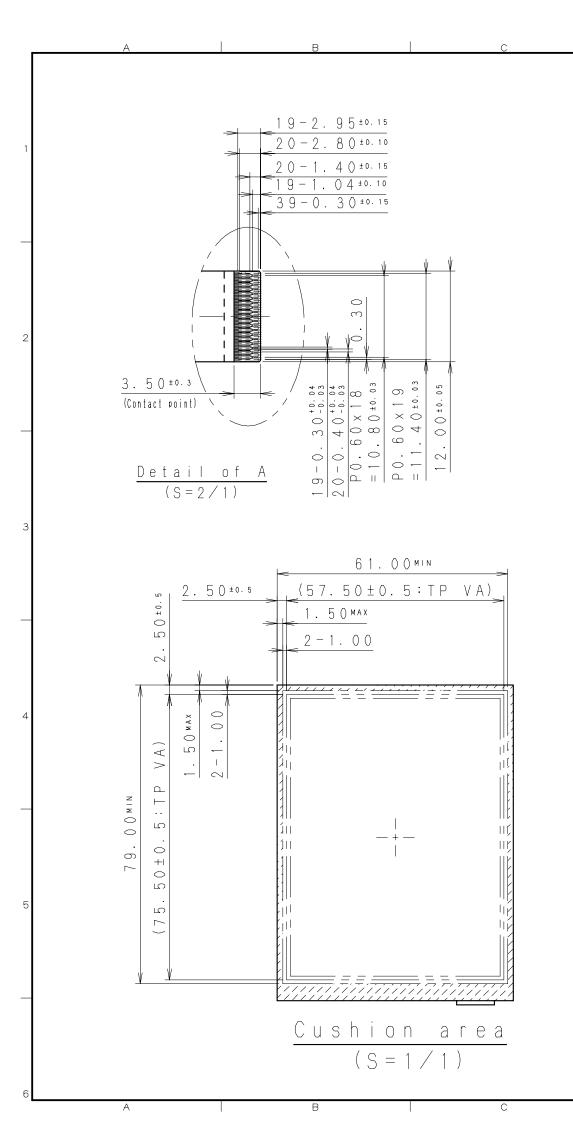


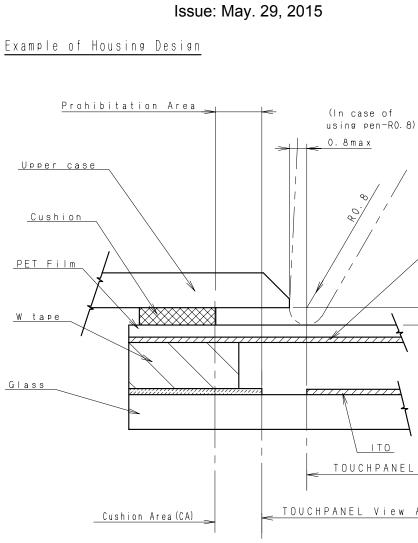
### 3. DIMENSIONS AND SHAPE

### 3.1 Dimensions

Items	Specifications	Unit	Remarks
Outline dimensions	63.50[H] × 85.00[V] × 4.33[D]	mm	Exclude FPC cable and parts on FPC.
Active area	53.64[H]×71.52[V]	mm	8.94cm diagonal
Number of dots	720[H]×320[V]	dot	
Dot pitch	74.5[H]×223.5[V]	μm	
Hardness of Touch Panel surface	3	Н	Load: 4.9N
Weight	43.5	g	Include FPC cable







### Design guidance for the u

Note 11. Upper case opening

D

a.Please place the upper b. The any pressures in th c.Please use the appropr

Note 12. Cushion design

a.Please put the cushion

- b.Do not use an adhesive
- c.Please position the cu:

E		F		G				<u>H</u>		
14 Issue: May. 2	(9/36) TLM101 29 2015	EC No.	REV.No.	REVIS	E	DATE (ү:и:D)	APPROVED (	HECKED PR	REPARED	
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Design										1
Area 🚽	usi	n case of ng pen-RO. 8max	8)							
				<u>I T O</u>						2
		UCHPANE NEL View		ve Area						n
JPPER CASE & t case opening to he area between T riate material(PM	maintain the P response a	operation rea and TP	viewing			sponse a	rea			4
n on the upper ca e tape to stick o ushion over the o	on the TP sul	to avoid								5
CHECKED CHECKED CHECKED	GENERAL TOLERANCE ± ( ISSUE (Y:W:D) 1 5 : 0 ! NAME	).5 <sup>s</sup> 5:20 <sup>m</sup>	<sup>cale</sup> re <sup>odel</sup> con	M35H3N99X**	ORTUS Do not dupli	TECHNO CATE, CONFI NG NC	LOGY C Dential A	O., LTI IND PROPRI	). Etary	
DESIG 税川晴久 DRAW 荒川晴久 E	0 U	TLINI F	E – D 3 T	N 9 9 g	RJD527	028D3		2/2 H		PART 6
							2 0	D4.2 DEVI	CE-TFT	

					(10/36)				
SPECIFICATIONS No. 14TLM101 Issue: May									
3.3 Seria	al Lat	oel (S-Lal	oel)						
	bel in	dicates the		ficant digit of manufacture year (1 digit), manufacture m (5 characters), serial number (6 digits).	onth with below				
* Co	ontent	s of Displa	ay						
*	*	*****	*****						
а	b	С	d						
	1								

	Contents of display							
а	The least significant	The least significant digit of manufacture year						
b	Manufacture month	Jan-A Feb-B Mar-C	May-E Jun-F Jul-G	Sep-I Oct-J Nov-K				
		Apr-D	Aug-H	Dec-L				
С	Model code	35MNC (Made i 35MPC (Made i						
d	Serial number	-						

\* Example of indication of Serial label (S-label)

•Made in Japan

5K35MNC000125

means "manufactured in November 2015, 3.5" MN type, C specifications, serial number 000125"

·Made in Malaysia

5K35MPC000125

means "manufactured in November 2015, 3.5" MP type, C specifications, serial number 000125"

2) Location of Serial Label (S-label) Refer to "3.2 Outward Form".

SPECIFICATIONS No. 14TLM101

### 4. PIN ASSIGNMENT

No.	Symbol	Functions
1	VSS	Ground
2	VSS	Ground
3	VDD	Power supply
4	VDD	Power supply
5	VSS	Ground
6	RESETB	Reset signal. When RESETB is Lo, an internal reset is performed.
7	HSYNC	Horizontal sync signal input. (Low active)
8	VSYNC	Vertical sync signal input. (Low active)
9	CLK	Clock signal for data latching and internal counter of the timing controller
10	VSS	Ground
11	D00	
12	D01	Display data(B)
13	D02	00h: Black
14	D03	D00:LSB D05:MSB
15	D04	Driver has internal gamma conversion.
16	D05	
17	D10	
18	D11	Display data(G)
19	D12	00h: Black
20	D13	D10:LSB D15:MSB
21	D14	Driver has internal gamma conversion.
22	D15	
23	D20	
24	D21	Display data(R)
25	D22	00h: Black
26	D23	D20:LSB D25:MSB
27	D24	Driver has internal gamma conversion.
28	D25	
29	VSS	Ground
30	DE	Input data effective signal. (It is effective for the period of "H")
31	STBYB	Standby signal (Hi:Normal operation, Lo:Standby operation)
32	TEST1	Connect to Ground.
33	XL	X-axis left terminal
34	YD	Y-axis downside terminal
35	XR	X-axis right terminal
36	YU	Y-axis upside terminal
37	TEST2	Connect to Ground.
38	BLH	LED drive power source (Anode side)
39	BLL	LED drive power source (Cathode side)

- Recommended connector: HIROSE ELECTRIC FH23 series [FH23-39S-0.3SHW(05)]

Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.
Please refer to the section 3.2 "Outward Form" for pin assignment.

#### SPECIFICATIONS No. 14TLM101

### 5. ABSOLUTE MAXIMUM RATING

						VSS=0V
Item	Item Symbol Condition Rating		Unit	Applicable terminal		
	-		MIN	MAX		
Supply voltage	VDD	Ta=25° C	-0.3	4.6	V	VDD
Input voltage for logic	VI		-0.3	VDD+0.3	V	CLK,VSYNC,HSYNC,DE
						D[05:00],D[15:10],D[25:20]
						STBYB,RESETB
						TEST1,TEST2
LED forward current	IL	Ta = 25°C	—	35	mA	BLH - BLL
		Ta = 70°C	—	15		
Touch Panel input voltage	VIT		—	7.0	V	XL,YD,XR,YU
Storage temperature range	Tstg		-30	80	°C	
Storage humidity range	Hstg		Non condensi	ng in an		
			environmenta	moisture at		
			or less than 4	0°C90%RH		

### 6. RECOMMENDED OPERATING CONDITIONS

							VSS=0V
Item	m Symbol Condition Rating					Unit	Applicable terminal
	-		MIN	TYP	MAX		
Supply voltage	VDD		2.7	3.0	3.6	V	VDD
Input voltage for logic	VI		0	_	VDD	V	CLK,VSYNC,HSYNC DE,D[05:00] D[15:10],D[25:20] STBYB,RESETB TEST1,TEST2
Operational temperature range Note 1	Тор	Note 2	-20	+25	+70	°C	Touch panel surface temperature
Operating humidity	Нор	Ta ≦ 30°C	20	—	80	%	
range		Ta > 30°C	environme	Non condensing in an environmental moisture at or less than 30°C80%RH.			

Note 1: This monitor is operatable in this temperature range. With regard to optical characteristics, refer to the section 10 "Characteristics".

Note 2: Acceptable Forward Current to LED is up to 15mA, when Ta=+70°C. Do not exceed Allowable Forward Current shown on the chart below (Fig. 2).

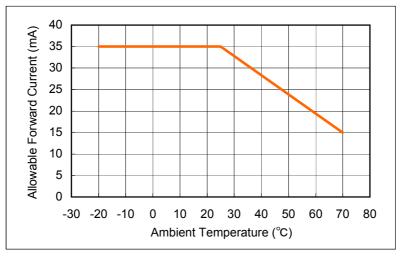


Fig. 2: Allowable Forward Current

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### 7. CHARACTERISTICS

### 7.1 Electrical Characteristics

### 7.1.1 Display Module

	(Unless otherwise hoted, Ta=25 C,VDD=3.0V,VSS=0V)										
Item	Symbol	Condition	Rating		Unit	Applicable terminals					
			MIN	TYP	MAX						
Input voltage	VIH		0.7×VDD	-	VDD	V	CLK,VSYNC,HSYNC				
for logic							DE,STBYB,RESETB				
	VIL		0	—	0.3×VDD	V	D[05:00],D[15:10],D[25:20]				
							TEST1,TEST2				
Operating	IDD	fCLK=6.25MHz	_	6.8	13.6	mA	VDD				
Current		Color bar display									

### 7.1.2 Backlight

Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Forward current	IL25	Ta=25°C	-	6.5	35.0	mA	BLH - BLL
	IL70	Ta=70°C	—	_	15.0	mA	
Forward voltage	VL	Ta=25°C, IL=6.5mA	-	16.0	16.7	V	
Estimated Life	LL	Ta=25°C, IL=6.5mA	-	(50,000)	—	hr	
of LED		Note1					

Note1: - The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.

- This figure is given as a reference purpose only, and not as a guarantee.
- This figure is estimated for an LED operating alone.
   As the performance of an LED may differ when assembled as a monitor together with a TFT panel due to different environmental temperature.
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

### 7.1.3 Touch Panel

							Ta=25 C
Item	Symbol	Condition		Rating			Applicable terminals
			MIN	TYP	MAX		
Linearity	LE	3mm in surroundings	-1.5	_	+1.5	%	
		Note is excluded					
Insulation	RI	DC 25V	20			MΩ	XL,XR – YD,YU
resistance	RI -		20	_	—	IVI 52	$\Lambda L, \Lambda R = TD, TO$
Terminal		Х	200	_	900	Ω	XL,XR
resistance		Y	200	—	900		YD,YU
Rated voltage		DC	—	5	7	V	XL,YD,XR,YU
on/off		R 0.8mm		_	10	me	XL,YD,XR,YU
chattering		Polyacetal pen	_		10	ms	$\Lambda L, I D, \Lambda R, I U$

Note: Linearity Measurement: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics". Load:2.45N

### Mechanical Reliability

Item		Rating		Unit	Remark		
	MIN	TYP	MAX				
Detectable activation force	0.05	_	0.80	N	R0.8mm Polyacetal pen or finger Resistance between X and Y axis must be equal or lower than 2KΩ.		
Keystroke durability	1,000,000	_	_	times	key the same part by silicon rubber (Touch Panel Active area only) •Rubber tip part: R8mm •Load: 2.50N •speed: 2 times/second		

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(Unless otherwise noted, Ta=25°C,VDD=3.0V,VSS=0V)

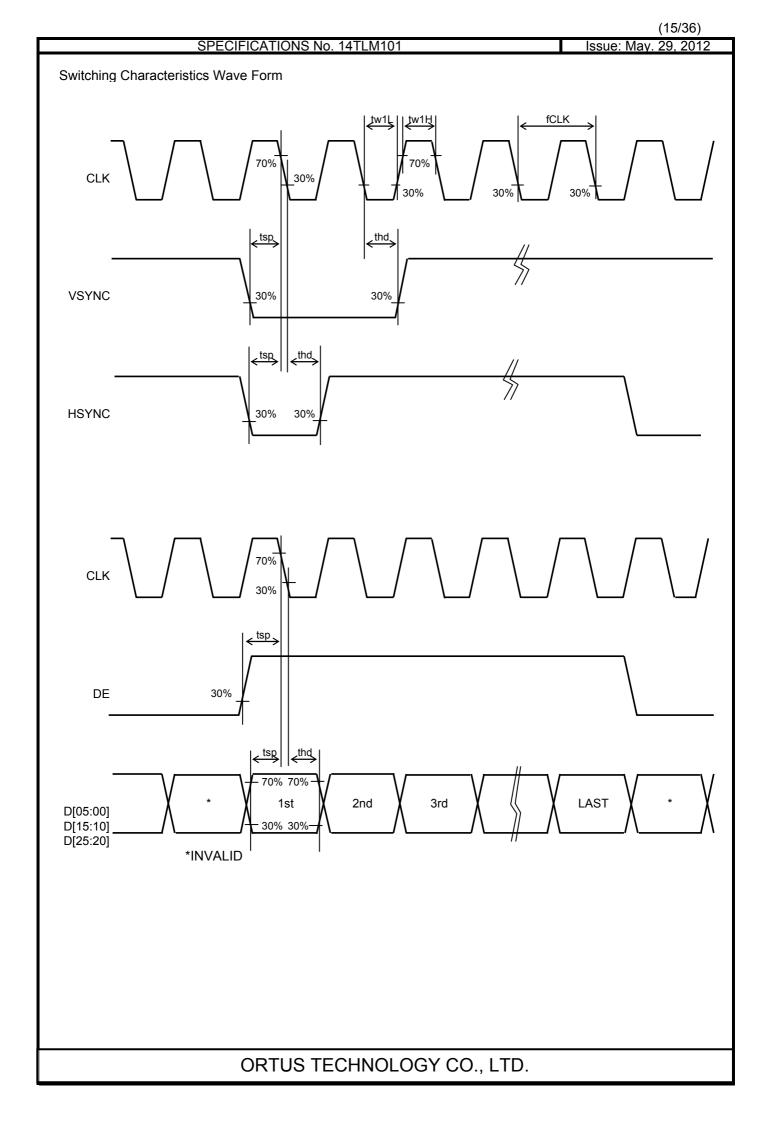
Ta=25° C

### SPECIFICATIONS No. 14TLM101

### 7.2 AC Characteristics

(Unless otherwise noted, Ta=25°C,VDD=3.0V,VSS=0V)

Item	Symbol	Condition		Rating			Applicable terminal
			MIN	TYP	MAX		
Clock frequency	fCLK		4.4	5.6	7.0	MHz	CLK
Clock Low period	tw1L	0.3×VDD or shorter	15	—	—	ns	CLK
Clock High period	tw1H	0.7×VDD or longer	15	_	_	ns	CLK
INPUT setup time	tsp		15	_	—	ns	CLK,VSYNC,HSYNC
							DE,D[05:00],STBYB
INPUT hold time	thd		15	—	—	ns	D[15:10],D[25:20]



### 8. Input Timing

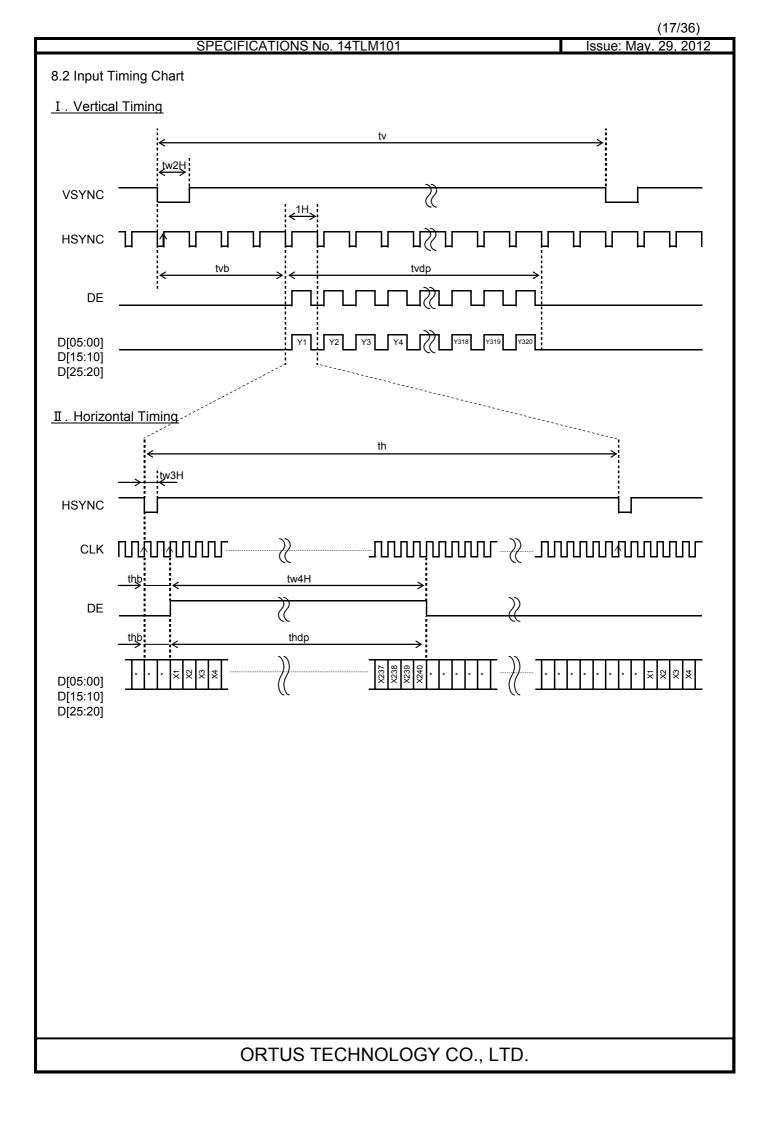
### 8.1 Input Timing Characteristics

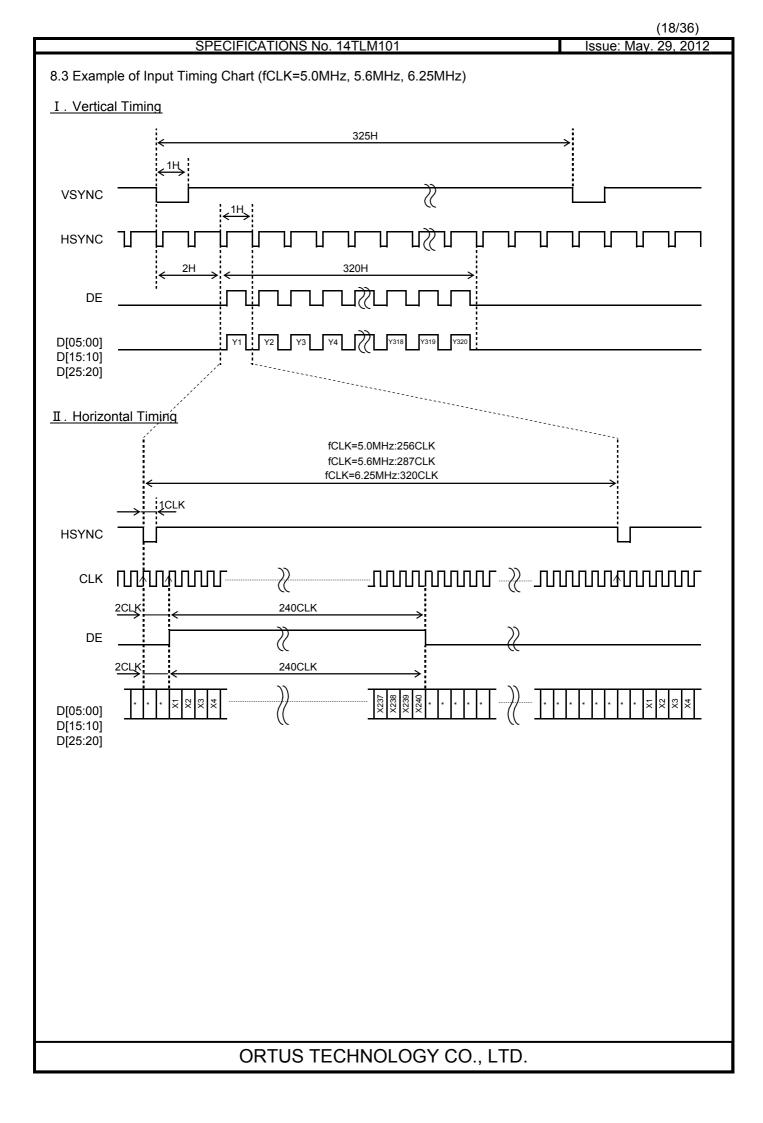
(Unless otherwise noted, Ta=25°C,VDD=3.0V,VSS=0V)

Item	Symbol		Rating		Unit	Applicable terminals
	-	MIN	TYP	MAX	1	
CLK frequency	fCLK	4.4	5.6	7.0	MHz	CLK
VSYNC frequency Note1	<b>fVSYNC</b>	54	60	66	Hz	VSYNC
VSYNC signal cycle time	tv	324	325	348	Н	VSYNC,HSYNC
VSYNC pulse width	tw2H	1	_		Н	VSYNC,HSYNC
Vartical back porch	tvb	2		14	н	VSYNC,HSYNC,DE
	1VD	2		14	11	D[05:00],D[15:10],D[25:20]
Vartical display period	tvdp	_	320	_	н	VSYNC,HSYNC,DE
vartical display period	tvup	_	520		11	D[05:00],D[15:10],D[25:20]
HSYNC frequency	fHSYNC	—	19.5		kHz	HSYNC
HSYNC signal cycle time	th	—	287	402	CLK	HSYNC,CLK
HSYNC pulse width	tw3H	1	_		CLK	HSYNC,CLK
Horizontal back porch	thb	2		14	CLK	CLK,HSYNC,DE
	uno	2		14	OLK	D[05:00],D[15:10],D[25:20]
DE pulse width	tw4H	_	240	_	CLK	DE,CLK
Horizontal display period	thdp		240	_	CLK	CLK,DE
	ulap		240		OLI	D[05:00],D[15:10],D[25:20]

Note 1: The characteristic of this item is recommended standard.

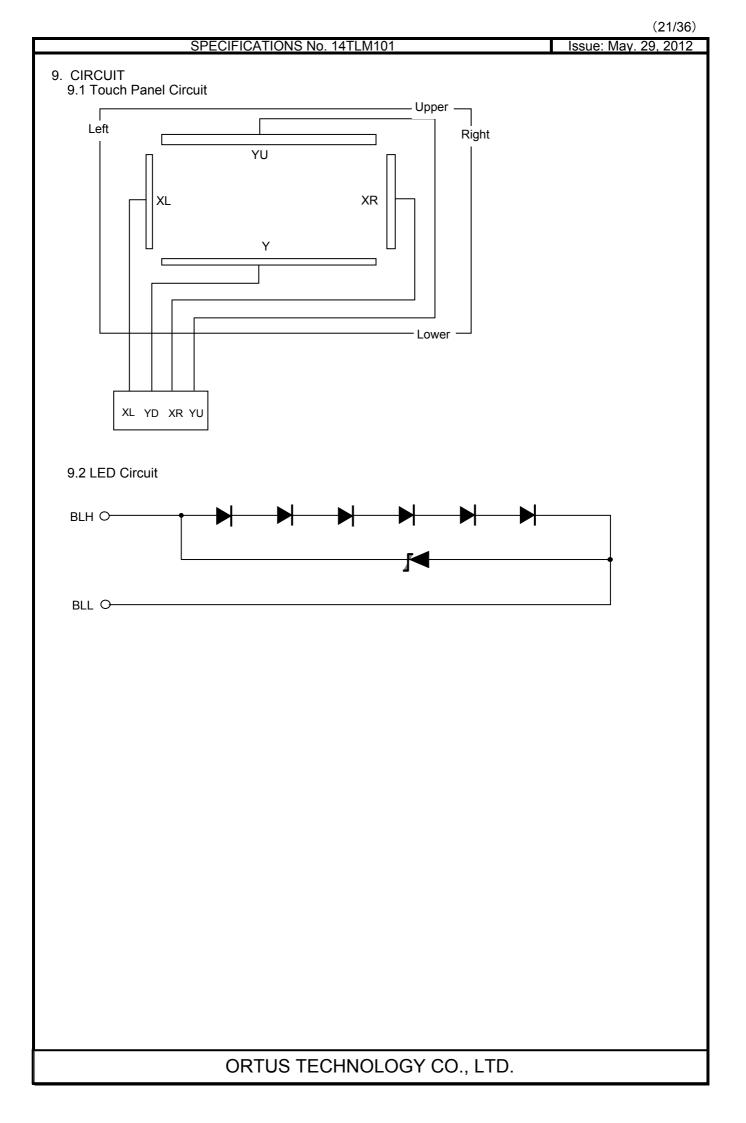
Please use it after it confirms it enough like the display fineness etc. when it comes off from this characteristic and it is used.





SPECIFICATIONS No. 14TLM101 Issue: May, 20.2012 8.4 Power ON/OFF sequence VDD VTC 11 VSYNC 12 VSYNC 1			(19/36)
VDD	0.4 5		issue: May. 29, 2012
RESETS STBYB STBYB USYNC 11 USYNC 11 USYN		UN/UFF sequence	
STBYB URL 10		—/ Min 1ms*2	
VSYNC *1 			Over 20 frame *4
VSYNC '1 LK '1 HSYNC DE DISP ON	STBYB	Min 0ms *3	<u>▶</u>
HSYNC	VSYNC *1	<u>1 2 3 4 5 6 7 26 27 28 29 30 31 32</u> <u>1 2 3 4 5 6</u>	19 20 21
HSNC	CLK *1		WMM
DE	HSYNC		
DISP ON Use A contract of the provided and the provide	DE		
bisplay OFF     Standby mode IN     CLK=6.26MHz ::26frame     CLK=5.60MHz ::26frame     CLK=5.60MHz ::3frame     CLK=5.00MHz ::3frame     CLK=5.00MHz ::3frame     CLK=5.00MHz ::3frame     CLK=5.00MHz ::3frame     CLK=5.00MHz ::20frame     CLK=5.00MHz ::20frame     CLK=5.00MHz ::3frame     CLK=5.00MHz     :1dfama     CLK=5.00MHz     :1dfama     it is eccessary to fix each signal is supplied from RESETB"H*     But meanwhile, It is necessary to fix each signal to "H"orL". *1 tis necessary to supply VSYNC and CLK(DOTCLK) for 20 frames or less from STBYB "L* to     turning off the power supply without leaving the afterimage.	DISP ON		
<ul> <li>Arbitrophysical and the power supply VSVNC and CLK(D0TCLK) for 20 frame or less from STBYB "L" to turning off the power supply without leaving the afterimage.</li> <li>1 to compare the power supply viethout leaving the afterimage.</li> <li>1 to compare the power supply viethout leaving the afterimage.</li> <li>1 to compare the power supply viethout leaving the afterimage.</li> <li>1 to compare the power supply viethout leaving the afterimage.</li> </ul>	2.0. 0		
Ick=625MHz       :28frame       Ick=625MHz       :14frame         Ick=6250MHz       :28frame       Ick=625MHz       :16frame         Ick=6250MHz       :38frame       Ick=50MHz       :16frame         Ick=625MHz       :38frame       Ick=50MHz       :16frame         Ick=625MHz       :38frame       Ick=50MHz       :16frame         Ick=625MHz       :38frame       Ick=625MHz       :16frame         Ick=625MHz       :28frame       Ick=625MHz       :16frame         Ick=620MHz       :28frame       Ick=620MHz       :20frame			
LK=500MHz       :23frame DLK=500MHz       CLK=500MHz       :20frame DLK=4.40MHz         *1       DOTCLK is used for Gate array Sinside counter. It becomes the operation after CLK(DOTCLK), VSYNC input.         *2       After the power supply,Please excute RESETB.(8.3 Reset sequence Reference)         *3       There is no regulations at time until each signal is supplied from RESETB'H" But meanwhile, it is necessary to fix each signal to "H'ort".         *4       It is necessary to supply VSYNC and CLK(DOTCLK) for 20 frames or less from STBYB "L" to turning off the power supply without leaving the afterimage.		CLK=6.25MHz : 26frame CLK=6.	25MHz :14farme
<ul> <li>*1 DDTCLK is used for Gate array CLK on FPC. VSYNC is used for Gate array's inside counter. It becomes the operation after CLK(DOTCLK),VSYNC input.</li> <li>*2 After the power supply,Please excute RESETB.(8.3 Reset sequence Reference)</li> <li>*3 There is no regulations at time until each signal is supplied from RESETB"H" But meanwhile, it is necessary to fix each signal to "H"or"L".</li> <li>*4 It is necessary to supply VSYNC and CLK(DOTCLK) for 20 frames or less from STBYB "L" to turning off the power supply without leaving the afterimage.</li> </ul>			
VSYNC is used for Gate array's inside counter. It becomes the operation after CLK(DOTCLK), VSYNC input. *2 After the power suply, Please excute RESETB.(8.3 Reset sequence Reference) *3 There is no regulations at time until each signal is supplied from RESETB"H" But meanwhile, It is necessary to fix each signal to "H"or"L". *4 It is necessary to supply VSYNC and CLK(DOTCLK) for 20 frames or less from STBYB "L" to turning off the power supply without leaving the afterimage.		CLK=4.40MHz :37frame CLK=4.	40MHz : 20frame
<ul> <li>*3 There is no regulations at time until each signal is supplied from RESETB"H" But meanwhile, It is necessary to fix each signal to "H"or"L".</li> <li>*4 It is necessary to supply VSYNC and CLK(DOTCLK) for 20 frames or less from STBYB "L" to turning off the power supply without leaving the afterimage.</li> </ul>	*	VSYNC is used for Gate array's inside counter.	
But meanwhile, It is necessary to fix each signal to "H"or"L". *4 It is necessary to supply VSYNC and CLK(DOTCLK) for 20 frames or less from STBYB "L" to turning off the power supply without leaving the afterimage.	**	2 After the power suplly, Please excute RESETB. (8.3 Reset sequence Reference)	
turning off the power supply without leaving the afterimage.	*;		
	*2		TBYB "L" to

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8.5 Display ON/OFF Sequence	
It explains the display sequence when display ON/OFF by the STBYB signal. The following time will be needed according to the CLK cycle by the time the displayis b from the standby release.	egun
STBYB 26frame : CLK 28frame : CLK 32frame : CLK 32frame : CLK	=5.60MHz =5.00MHz
VSYNC	=4.40MHz
DATA Display OFF Display 0	NC
Backlight OFF	ON
The following time will be needed according to the CLK cycle by the time the standby sequence is ended from the standby setting. Meanwhile, DOTCLK and the VSYNC signal should keep being supplied. When DOTCLK and the VSYNC signal are stopped or the power supply is turned off to frame or less, the afterimage might remain.	a regulated
STBYB 14frame : CLK 16frame : CLK 18frame : CLK	=5.60MHz =5.00MHz
VSYNC	=4.40MHz
DATA Display ON Display OFF Standby Ir	1
Backlight ON OFF	
8.6 Reset Sequence	
There is a limitation between the power supply turning on and the RESETB input. Please defend the following conditions. 90%	
RESETB	
T>1ms	
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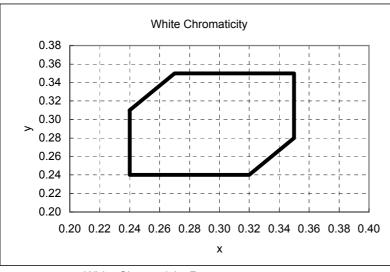
### SPECIFICATIONS No. 14TLM101

### **10. CHARACTERISTICS**

10.1 Optical Character	istics
< Measurement Condition	1>
Measuring instruments:	CS1000 (KONICA MINOLTA), LCD7200(OTSUKA ELECTRONICS),
	EZcontrast160D(ELDIM)
Driving condition:	VDD = 3.0V, VSS = 0V
	Optimized VCOMDC
Backlight:	IL=6.5mA
Measured temperature:	$Ta = 25^{\circ}C$

	Item	Symbol	Condition	MIN	TYP	MAX	Unit	Note No.	Remark
Response time	Rise time	TON	[Data]= 3Fh→00h	-	-	40	ms	1	*
Resp tin	Fall time	TOFF	[Data]= 00h→3Fh	-	-	60	ms		
Contrast ratio	Backlight ON	CR	[Data]= 3Fh/00h	240	400			2	
Cont	Backlight OFF			-	1.9	-			
b	Left	θL	[Data]=	80	—		deg	3	*
Viewing angle	Right	θR	3Fh/00h	80	—	1	deg		
/ie/	Up	φU	CR≧10	80	—	_	deg		
_	Down	φD		80	—	—	deg		
White	e Chromaticity	Х	[Data]=3Fh	White ch	nromatici	ty range		4	
		у			<u> </u>			_	
Burn-	, and the second s		•	5					
	be observed after 2 hours of								
					window pattern display.				
Cente	er brightness		[Data]=3Fh	130	190	—	cd/m <sup>2</sup>	6	
Brigh	tness distributi	on	[Data]=3Fh	70	—	—	%	7	

#### SPECIFICATIONS No. 14TLM101



[White Chromaticity Range]

	-
Х	У
0.24	0.31
0.24	0.24
0.32	0.24
0.35	0.28
0.35	0.35
0.27	0.35

White Chromaticity Range

10.2 Temperature Characteristics

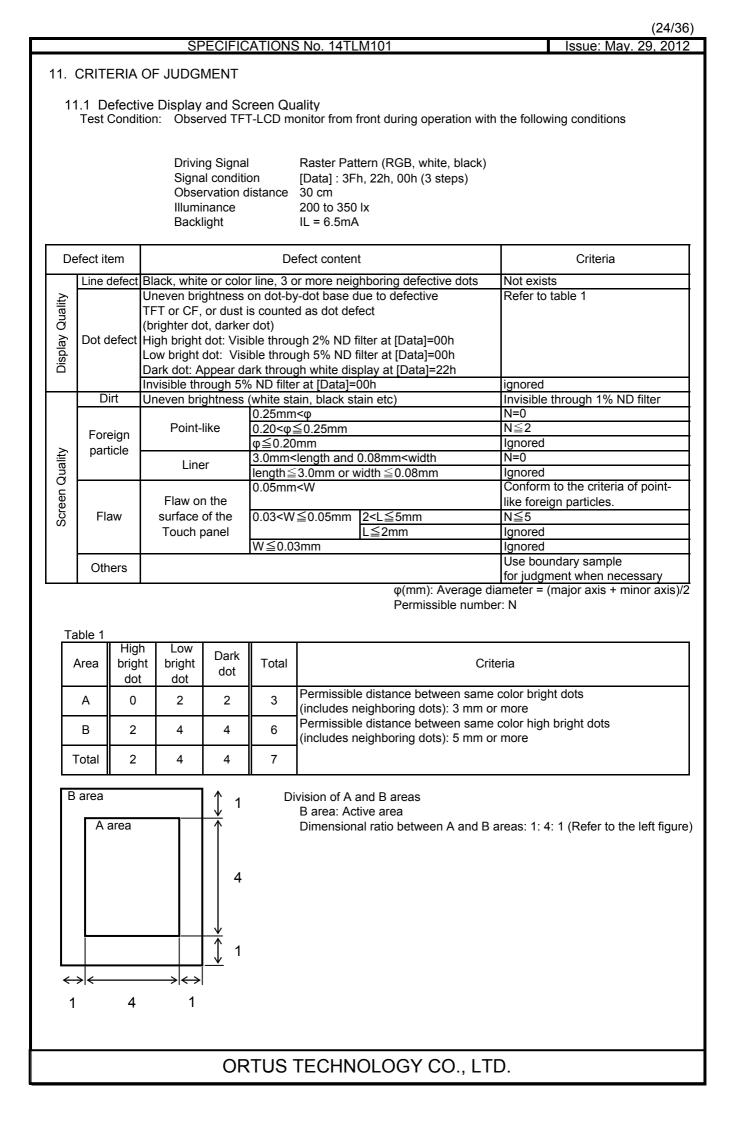
< Measurement Condition > Measuring instruments: CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS) Driving condition: VDD = 3.0V, VSS = 0V Optimized VCOMDC Backlight: IL=6.5mA

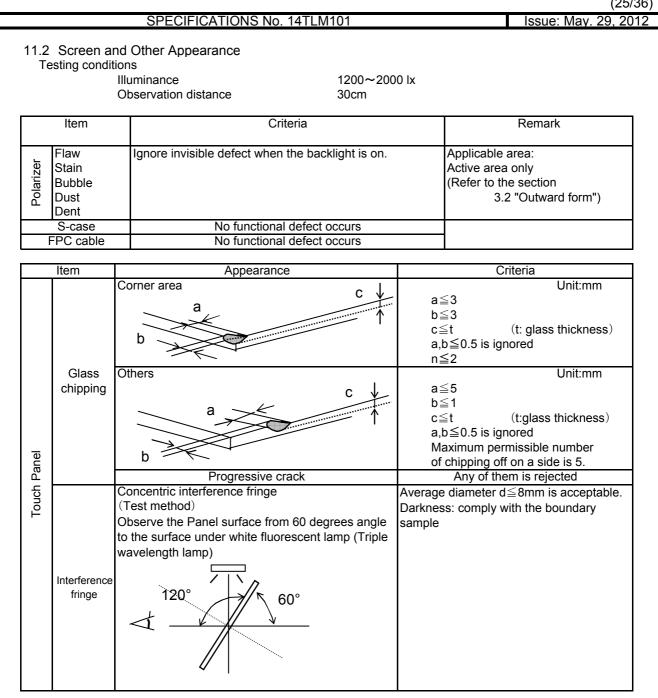
Item			Specification		Remark
1	lem		Ta=-20° C	Ta=70° C	Remark
Contr	ast ratio	CR	40 or more	40 or more	Backlight ON
Response time	Rise time	TON	200 msec or less	30 msec or less	*
rtesponse time	Fall time	TOFF	300 msec or less	50 msec or less	*
Display Quality			No noticeable display o should be observed.	defect or ununiformity	Use the criteria for judgment specified in the section 11.

※ Measured in the form of LCD module.

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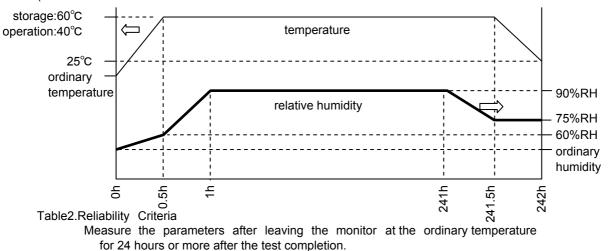


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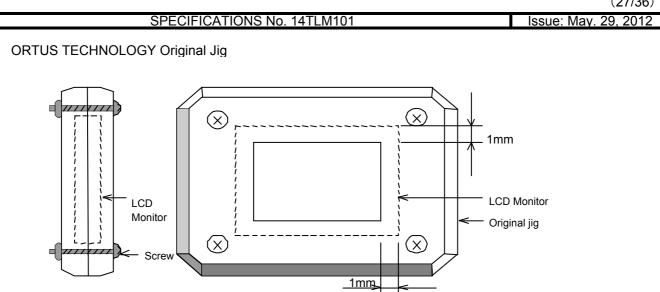
	SPECIFICATIC	NS No. 14TLM101	Issue: May. 29, 2
RELL	ABILITY TEST		
	Test item	Test condition	number of failures /number of examination
	High temperature storage	Ta=80° C 240hr	0/3
	Low temperature storage	Ta=-30° C 240hr	0/3
est	High temperature & high	Ta=60° C, RH=90% 240hr	0/3
Durability test	humidity storage	non condensing 🛛 💥	
ilit	High temperature operation	Tp=70° C 240hr	0/3
rab	Low temperature operation	Tp=-20° C 240hr	0/3
Du	High temp & humid operation	Tp=40°C, RH=90% 240hr non condensing	0⁄3
	Thermal shock storage	-30←→80° C(30min/30min) 100 cycles	0/3
al test	Electrostatic discharge test (Non operation)	Confirms to EIAJ ED-4701/300 C=200pF,R=0Ω,V=±200V Each 3 times of discharge on and power supply and other terminals.	0⁄3
Mechanical environmental test	Surface discharge test (Non operation)	C=250pF, R=100 $\Omega$ , V=±12kV Each 5 times of discharge in both polarities on the center of screen with the case and Touch Panel terminal grounded.	0⁄3
cal en	Vibration test	Total amplitude 1.5mm, f=10 $\sim$ 55Hz, X,Y,Z directions for each 2 hours	0⁄3
Mechani	Impact test	Use ORTUS TECHNOLOGY original jig (see next page) and make an impact with peak acceleration of 1000m/s <sup>2</sup> for 6 msec with half sine-curve at 3 times to each X, Y, Z directions in conformance with JIS 60068-2-27-2011.	0⁄3
Packing test	Packing vibration-proof test	Acceleration of 19.6m/s <sup>2</sup> with frequency of $10 \rightarrow 55 \rightarrow 10$ Hz, X,Y, Zdirection for each 30 minutes	0∕1 Packing
Pack	Packing drop test	Drop from 75cm high. 1 time to each 6 surfaces, 3 edges, 1 corner	0∕1 Packing

\* The profile of high temperature/humidity storage and High Temperature/humidity operation

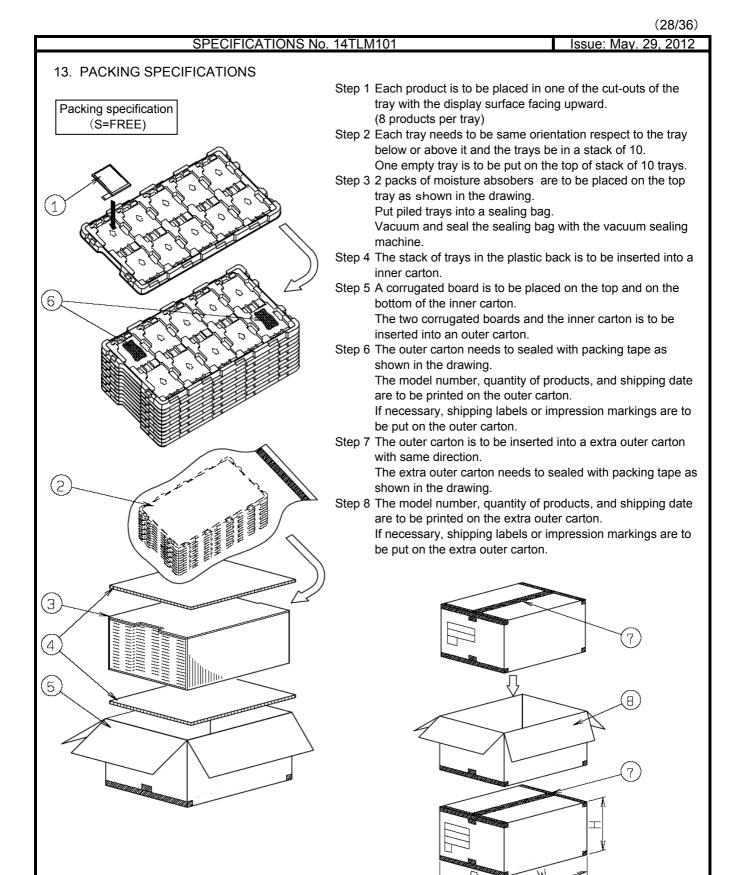
(Pure water of over  $10M\Omega \cdot cm$  shall be used.)



Item	Standard	Remarks
Display quality	No visible abnormality shall be seen.	As criteria of 11 "CRITERIA OF JUDGMENT".
Contrast ratio	40 or more	Backlight ON



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Remark: The return of packing materials is not required.

	· · · · · · · · · · · · · · · · · · ·	
	Packing item name	Specs., Material
1	Tray	PP Conductive
2	Inner carton	Corrugated cardboard
3	Inner board	Corrugated cardboard
4	Outer carton	Corrugated cardboard
5	Sealing bag	
6	Drier	Moisture absorber
$\bigcirc$	Packing tape	
8	Extra outer carton	Corrugated cardboard

Dimension of extra outer carton		
D : Approx. (338mm)		
W : Approx.	(549mm)	
H : Approx.	(198mm)	
Quantity of products	10pcs×10=100pcs	
packed in one carton		
Gross weight : Approx.	7.7Kg	

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	Caution
(1)	Do not make an impact on the LCD panel glass because it may break and you may get injured from it.
(2)	If the glass breaks, do not touch it with bare hands. (Fragment of broken glass may stick you or you cut yourself on it.
(3)	If you get injured, receive adequate first aid and consult a medial doctor.
(4)	Do not let liquid crystal get into your mouth. (If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.
(5)	If liquid crystal adheres, rinse it out thoroughly. (If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap. If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.
(6)	If you scrap this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.
(7)	Do not connect or disconnect this product while its application products is powered on.
(8)	Do not attempt to disassemble or modify this product as it is precision component.
(9)	If a part of soldering part has been exposed, and avoid contact (short-circuit) with a metallic part of the case etc. about FPC of this model, please. Please insulate it with the insulating tape etc. if necessary. The defective operation is caused, and there is a possibility to generation of heat and the ignition.
(10)	Since excess current protection circuit is not built in this TFT module, there is the possibility that LCD module or peripheral circuit become feverish and burned in case abnoramal operation is generated We recommend you to add excess current protection circuit to power supply.
(11)	The end part of glass and film of touch panel has conductivity, and avoid contact (short-circuit) with electroconductive case etc There is a possibility of setting up a defective touch panel, and insulate it for the case suppression (cushion etc.) if necessary, please.
	Caution This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.

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14.2	Precautions for Handling	
1)	Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean. Do not touch the surface of the monitor as it is easily scratched.	
2)	Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent st charge and discharge when handling the TFT monitors as the LED in this TFT monitor is damageable to electrostatic discharge, Properly set up equipment, jigs and machines, and keep working area clean and tidy for handling the TFT monitors.	ors
3)	Avoid strong mechanical shock including knocking, hitting or dropping to the TFT mo for protecting their glass parts. Do not use the TFT monitors that have been experied dropping or strong mechanical shock.	
4)	Do not use or storage the TFT monitors at high temperature and high humidity environ Particularly, never use or storage the TFT monitors at a location where condensation	
5)	Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet	rays.
6)	Do not stain or damage the contacts of the FPC cable . FPC cable needs to be inserted until it can reach to the end of connector slot. During insertion, make sure to keep the cable in a horizontal position to avoid an obli Otherwise, it may cause poor contact or deteriorate reliability of the FPC cable.	que insertion.
7)	The FPC cable is a design very weak to the bend and the pull as it is fixed with the ta Do not bend or pull the FPC cable or carry the TFT monitor by holding the FPC cable	
8)	Peel off the protective film on the TFT monitors during mounting process. Refer to the section 14.5 on how to peel off the protective film. We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.	
14.3	Precautions for Operation	
1)	Since this TFT monitors are not equipped with light shielding for the driver IC, do not expose the driver IC to strong lights during operation as it may cause function	al failures.
2)	In case of powering up or powering off this LCD module, be sure to comply the sequence as instructed in this specification.	
3)	Do not plug in or out the FPC cable while power supply is switch on. Plug the FPC cable in and out while power supply is switched off.	
4)	Do not operate the TFT monitors in the strong magnetic field. It may break the TFT ${\sf r}$	nonitors.
5)	Do not display a fixed image on the screen for a long time. Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time. Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.	

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Storage environment	
Temperature	0 to 40°C
Humidity	60%RH or less
-	No-condensing occurs under low temperature with high humidity condition.
Atmosphere	No poisonous gas that can erode electronic components and/or wiring materials should be detected.
<ul> <li>Time period</li> </ul>	3 months
• Unpacking	To protect the TFT monitors from static damage during unpacking, keep room humidity more than 50%RH and implement effective countermeasures against static electricity such as establishing a ground (an earth) before unpacking.
<ul> <li>Maximum piling up</li> </ul>	7 cartons

#### 14.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

#### A) Work Environment

- a) Humidity: 50 to 70 %RH, Temperature15°C to 27°C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps. Anti-static treatment should be implemented to work area's floor.
- c) Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.

#### B) Work Method

The following procedures should taken to prevent the driver ICs from charging and discharging.

- a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower left when the LCD-FPC cable is facing to the leftside.
   Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
- b) Put an adhesive tape (Scotch tape, etc) at the lower left corner area of the protective film to prevent scratch on surface of TFT monitors.
- c) Peel off the adhesive tape slowly (spending more than 2 secs to complete) by pulling it to opposite direction.

Direction of blowing air (Optimize air direction and the distance)

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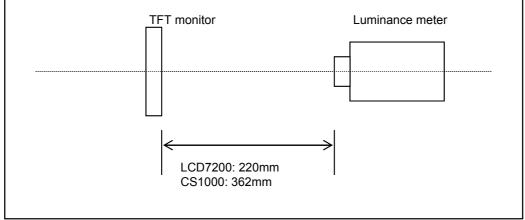
Issue: May. 29, 2012

APPENDIX

Reference Method for Measuring Optical Characteristics and Performance

1. Measurement Condition	on (Backlight ON)
Measuring instruments:	CS1000(KONICA MINOLTA), LCD7200(OTSUKA ELECTRONICS), EZcontrast160D(ELDIM)
Driving condition:	Refer to the section "Optical Characteristics"
Measured temperature:	25°C unless specified
Measurement system:	See the chart below. The luminance meter is placed on the normal line of
	measurement system.
Measurement point:	At the center of the screen unless otherwise specified

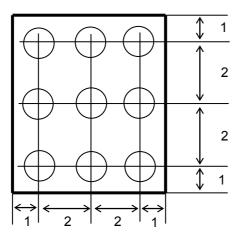
Dark box at constant temperature



Measurement is made after 30 minutes of lighting of the backlight.

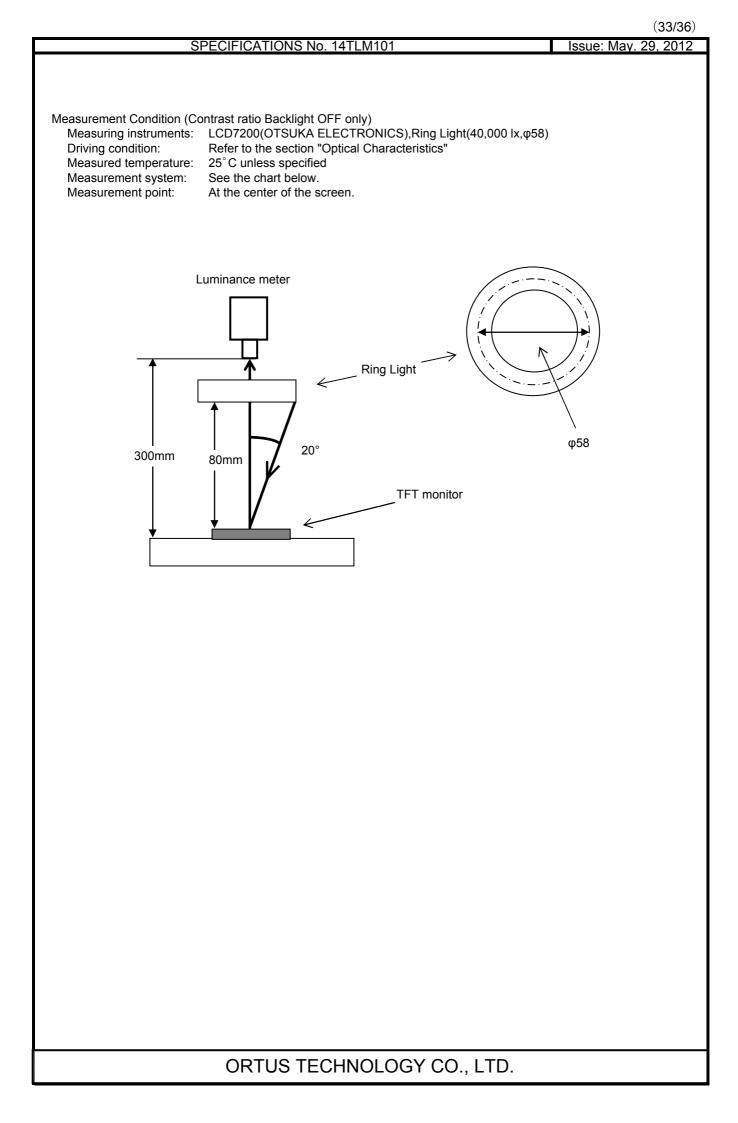
Measurement point:

At the center point of the screen Brightness distribution: 9 points shown in the following drawing.



Dimensional ratio of active area

Backlight IL = 6.5mA



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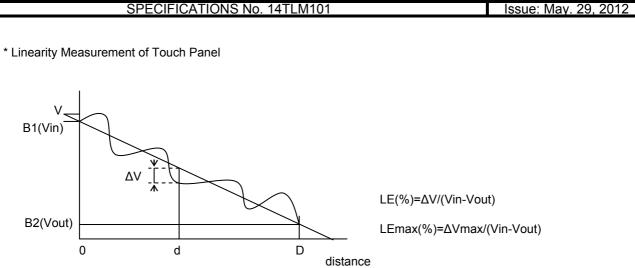
			(34/30)			
ssue:	May	/.	29.	2012		

Notice	Item	Test method	Measuring instrument	Remark
1 Response time	Measure output signal waveform by the luminance meter when raster of window pattern is changed from white to black and from black to white.	LCD7200	Black display [Data]=00h White display [Data]=3Fh	
		White Black White		TON Rise time TOFF Fall time
		100% 90%		
		10% 0% Black TON TOFF		
2	Contrast ratio	Measure maximum luminance Y1([Data]=3Fh) and minimum luminance Y2([Data]=00h) at the center of the screen by displaying raster or window pattern. Then calculate the ratio between these two values. Contrast ratio = Y1/Y2 Diameter of measuring point: 8mm $\phi$ (CS1000) Diameter of measuring point: 3mm $\phi$ (LCD7200)	CS1000 LCD7200	Backlight ON Backlight OFF
3	Viewing angle Horizontal $\theta$ Vertical $\phi$	Move the luminance meter from right to left and up and down and determine the angles where contrast ratio is 10.	EZcontrast160D	
4	White	Measure chromaticity coordinates x and y of CIE1931 colorimetric system at [Data] = 3Fh Color matching faction: 2°view	CS1000	
5	Burn-in	Visually check burn-in image on the screen after 2 hours of "window display" ([Data]=3Fh/00h).		At optimized Vcom/C
6	Center brightness	Measure the brightness at the center of the screen.	CS1000	
7	Brightness distribution	(Brightness distribution) = 100 x B/A % A : max. brightness of the 9 points B : min. brightness of the 9 points	CS1000	

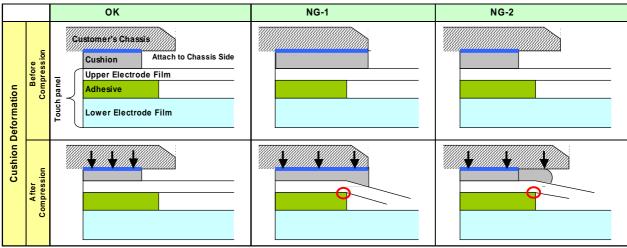
(34/36)

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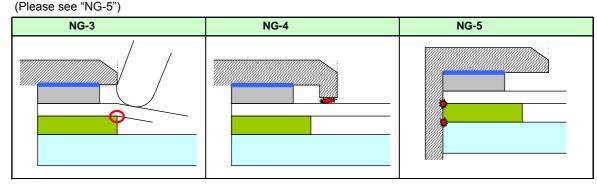
### SPECIFICATIONS No. 14TLM101



- Cautionary instruction to handle a Touch-panel
  - Cushion (between Touch Panel Chassis) Design
    - A cushion is required to be placed between Touch Panel and customer's chassis and there is a designated area to attach it. Attachment at area inside Input Prohibition Area must be forbidden. If cushion was located inside Input Prohibition Area, Upper Electrode may be push constantly and which may cause the electrode breakage at the position falling on the edge of adhesive; it eventually results in Touch Panel malfunction in the future. (Please see "NG-1")
    - Be attention to the cushion material you use. In the case that too soft cushion was used, the cushion may protrude into Prohibition Area by being push strongly; which may result in the electrode breakage. Eventually there is a chance that the electrode breakage leads to the malfunction of Touch Panel in the future. (Please see "NG-2")
    - Cushion is required to be attached at the side of Customer's chassis. Attaching a cushion at the side of Upper Electrode Film has a chance to deform the film and lead to the malfunction of Touch Panel in the future.



- Design Guidance of Chassis (Front Part)
  - Be attention to stay Input Prohibition Area away from touching and/or drawing by a stylus pens in order to avoid the electrode breakage and potential malfunction of Touch Panel. (Please see "NG-3") We recommend customers to design chassis (front case) being able to protect Input Prohibition Area.
  - Clearance between customer's chassis and Touch Panel surface is certainly required in order to avoid erroneous input caused by a collision of the edge of chassis. (Please see "NG-4") A clearance of 0.3 to 0.7mm is recommended.
- Design Guidance of Chassis (Side Part)
  - Opper Electrode and Lower Electrode fall on the edge of Touch Panel outline.
     Redundant design having enough clearance to avoid electric short with chassis is highly recommended.



- Example of Recommended Chassis Design Refer to "3.2 Outward Form".
- As a terminal resistance has individual specificity, calibration to align the displaying and the sensing position one each is mandatory before use.